

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket

ERNST BRETSCHNEIDER ET AL

DE 010005

Serial No.

Filed: CONCURRENTLY

CIRCUIT ARRANGEMENT AND METHOD OF PROTECTING AT LEAST A CHIP
ARRANGEMENT FROM MANIPULATION AND/OR ABUSE

Commissioner for Patents
Washington, D.C. 20231

BEST AVAILABLE COPY

PRELIMINARY AMENDMENT

Sir:

Prior to calculation of the filing fee and examination,
please amend the above-identified application as follows:

IN THE CLAIMS

Please amend the claims as follows:

3. A circuit arrangement (100) as claimed in claim 1,
characterized in that the detector unit (10) comprises at
least one bipolar transistor (12), particularly at least one
pnp transistor.

5. A circuit arrangement (100) as claimed in claim 3,
characterized in that the emitter (124) of the bipolar
transistor (12) is connected to the input (22), provided for
the output voltage (V_{out}), of the comparator unit (20).

6. A circuit arrangement (100) as claimed in claim 3,
characterized in that the emitter (124) of the bipolar

#B/A
PRE-AMEND
J. Macmillan
4/22/02

OFFICE OF THE COMMISSIONER OF PATENTS
WASHINGTON, D.C. 20231

A7
Lub
B2